



Certificate no: UC-543a8c09-b674-4bae-b074-ed37cd878693

Certificate url: ude.my/UC-543a8c09-b674-4bae-b074-ed37cd878693

Reference Number: 0004

CERTIFICATE OF COMPLETION

Verilog HDL: VLSI Hardware Design Comprehensive Masterclass

Instructors **Shepherd Tutorials**

Гулько Артем Олегович

Date **Sept. 26, 2024**

Length **12.5 total hours**